

ADDRESSING MODES



Immediate	Number hardcoded into instruction	<code>MOVE.L #5, D0</code>
Direct	Address hardcoded into instruction	<code>MOVE.L VAR, D0</code>
Register Indirect	Address comes from an address register	<code>MOVE.L (A0), D0</code>
Register Indirect + Postincrement	Address comes from an address register, which is incremented after the instruction completes	<code>MOVE.L (A0)+, D0</code>
Register Indirect + Index	Address comes from the sum of address register and data register	<code>MOVE.L (A0, D1), D0</code>

More
Hardcoded



More
Flexible

IMMEDIATE

Java


```
int i = 5;
```

ASSEMBLY

```
MOVE.L #5,D0
```

```
for (i = 0; i < 100; i++) {  
    .  
    .  
    .  
}
```

```
MOVE.L #0,D0
```



DIRECT

Java

```
int i = 5;
```

ASSEMBLY

```
MOVE.L #5, D0  
MOVE.L D0, I
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	00	00	00	00



I

DIRECT

Java

```
int i = 5;
```

ASSEMBLY

```
MOVE.L #5, D0  
MOVE.L D0, I
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	05
C	00	00	00	00



I

DIRECT

```
LOOP:  
  ADD.L #1, COUNT  
  CMPI.L #15, COUNT  
  BLT LOOP
```

```
COUNT:  
  DC.L 10
```

COUNT



0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	00	00	00	0C

REGISTER INDIRECT

Java

```
int i = 5;
```

ASSEMBLY

```
MOVE.L #5, D0  
LEA I, A0  
MOVE.L D0, (A0)
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	00	00	00	00

REGISTER INDIRECT

Java

```
int i = 5;
```

ASSEMBLY

```
MOVE.L #5, D0  
LEA I, A0  
MOVE.L D0, (A0)
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	05
C	00	00	00	00



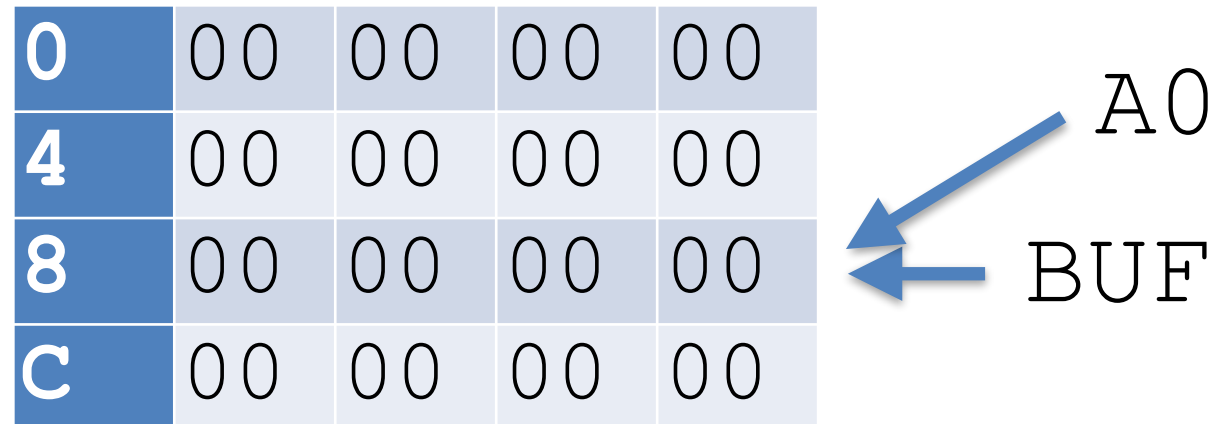
REGISTER INDIRECT

Java

```
char buf[10];  
buf[0] = 0xff;
```

ASSEMBLY

```
LEA BUF, A0  
MOVE.B #$FF, (A0)
```



REGISTER INDIRECT

Java

```
char buf[10];  
buf[0] = 0xff;
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.B #$FF,(A0)
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	FF
C	FF	FF	00	00

A0 = D

← BUF

REGISTER INDIRECT WITH INDEX

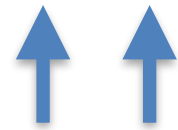
Java

```
char buf[10];  
for(int k = 0; k < 10; k++){  
    buf[0] = 0xff;  
}
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.L #0,D0  
MOVE.B #$FF,(A0,D0)  
ADDI.L #1,D0
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	00	00	00	00



BUF A0+D0

REGISTER INDIRECT WITH INDEX

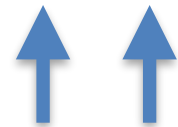
Java

```
char buf[10];  
for(int k = 0; k < 10; k++){  
    buf[0] = 0xff;  
}
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.L #0,D0  
MOVE.B #$FF,(A0,D0)  
ADDI.L #1,D0
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	FF	00	00	00



BUF A0+D0

REGISTER INDIRECT WITH INDEX

Java

```
char buf[10];  
for(int k = 0; k < 10; k++){  
    buf[0] = 0xff;  
}
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.L #0,D0  
MOVE.B #$FF,(A0,D0)  
ADDI.L #1,D0
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	FF	FF	00	00



BUF



A0+D0

REGISTER INDIRECT WITH INDEX

Java

```
char buf[10];  
for(int k = 0; k < 10; k++){  
    buf[0] = 0xff;  
}
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.L #0,D0  
MOVE.B #$FF,(A0,D0)  
ADDI.L #1,D0
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	FF	FF	FF	00

↑ ↑

BUF A0+D0

REGISTER INDIRECT WITH POSTINCREMENT

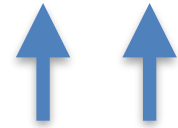
Java

```
char buf[10];  
for(int k = 0; k < 10; k++){  
    buf[0] = 0xff;  
}
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.L #0,D0  
MOVE.B #$FF,(A0)+
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	00	00	00	00



BUF A0

REGISTER INDIRECT WITH POSTINCREMENT

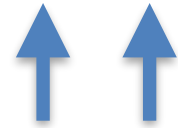
Java

```
char buf[10];  
for(int k = 0; k < 10; k++){  
    buf[0] = 0xff;  
}
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.L #0,D0  
MOVE.B #$FF,(A0)+
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	FF	00	00	00



BUF A0

REGISTER INDIRECT WITH POSTINCREMENT

Java

```
char buf[10];  
for(int k = 0; k < 10; k++){  
    buf[0] = 0xff;  
}
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.L #0,D0  
MOVE.B #$FF,(A0)+
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	FF	FF	00	00

↑ ↑
BUF A0

REGISTER INDIRECT WITH POSTINCREMENT

Java

```
char buf[10];  
for(int k = 0; k < 10; k++){  
    buf[0] = 0xff;  
}
```

ASSEMBLY

```
LEA BUF,A0  
MOVE.L #0,D0  
MOVE.B #$FF,(A0)+
```

0	00	00	00	00
4	00	00	00	00
8	00	00	00	00
C	FF	FF	FF	00



BUF



A0